



Feature

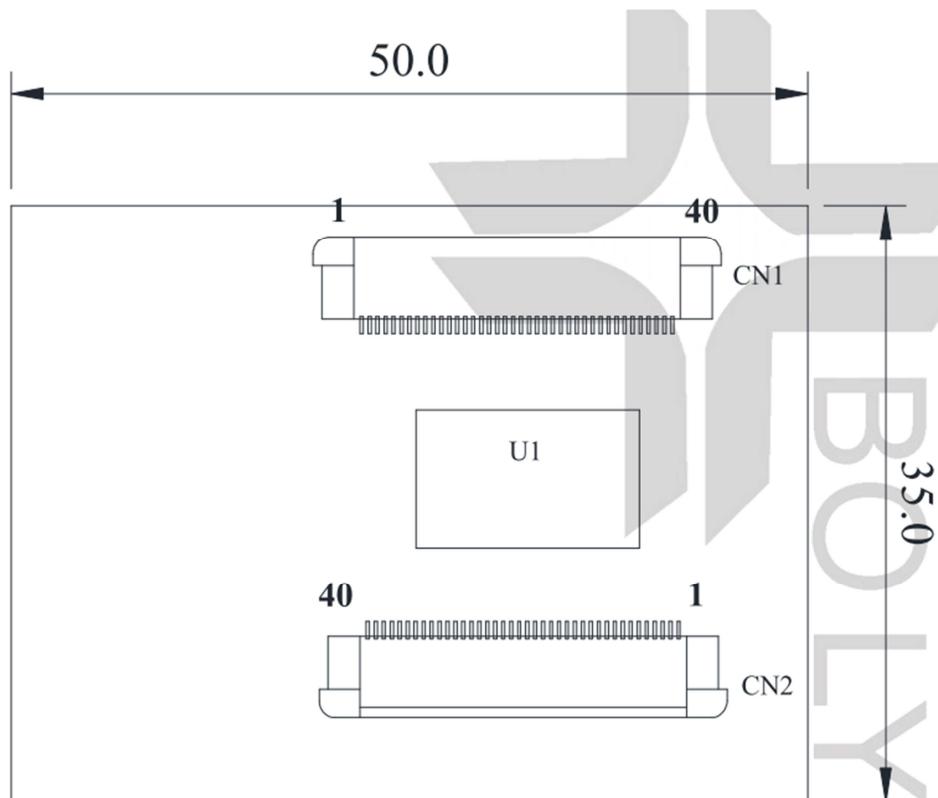
1. LVDS to RGB Interface board.
2. No Special Start-up Sequence Required
3. Pixel Clock Range: 8 to 112MHz
4. Operating temperature :-20 to 70 °C
5. Option: Backlight driving up to 200mA.

General Spec.

Module Dimension	50.0 x 35.0 mm
Driver IC	THC63LVDF84C
Input interface	LVDS
Output interface	RGB 24 BITS



Dimension



Pin assignment (Input)

Pin No.	Symbol	Function
1	NC	No connection.
2,3	VDD	Power supply input.
4~6	NC	No connection.
7	GND	Ground.
8	RA-	-LVDS differential data input.
9	RA+	+LVDS differential data input.
10	GND	Ground.
11	RB-	-LVDS differential data input.
12	RB+	+LVDS differential data input.
13	GND	Ground.
14	RC-	-LVDS differential data input.
15	RC+	+LVDS differential data input.
16	GND	Ground.
17	RCLK-	-LVDS differential clock input.
18	RCLK+	+LVDS differential clock input.
19	GND	Ground.
20	RD-	-LVDS differential data input.
21	RD+	+LVDS differential data input.
22	GND	Ground.
23,24	NC	No connection.
25	GND	Ground.
26~29	NC	No connection.
30	GND	Ground.
31,32	LED-	LED Cathode
33~38	NC	No connection.
39,40	LED+	LED Anode.

Pin assignment (Output)

Pin No.	Symbol	Function
1	LED-	LED Cathode
2	LED+	LED Anode.
3	GND	Ground.
4	VDD	Power supply input.
5~12	R0~R7	8-bit digital RED data output.
13~20	G0~G7	8-bit digital Green data output.
21~28	B0~B7	8-bit digital BLUE data output.
29	GND	Ground.
30	CLK	Pixel Clock signal
31	DISP	Output High.
32	HYSNC	Horizontal sync signal..
33	VSYNC	Vertical sync signal.
34	DEN	Data input enable.
35	NC	No connection.
36	GND	Ground.
37~40	NC	No connection.

Note 1: This board also could provide TFT panel backlight driving current by provide LED+ =5V & LED-=GND. Please contact Bolymin's sales for this function.